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JEDEC Type: 2N2757, 2758, 2759, 2760, 2761, 2762,

2N2763, 2764, 2765, 2766, 2767, 2768, 2N2769, 2770, 2771, 2772, 2773, 2774, 2N2775, 2776, 2777, 2778, 2779, 2780

JOINT ELECTRON DEVICE ENGINEERING COUNCIL REGISTRATION DATA SILICON POWER TRANSISTOR POWER SWITCHING

I. General Description

This transistor is a NPN silicon power transistor designed primarily for high power switching applications and inverters at or below the collector current given in Table I. It is intended for Industrial and Military class of service.

II. <u>Mechanical Data</u>

- The transistor has an outline as per Figure 1
- Terminal location is shown in Figure 1
- Maximum Stud Torque 40 in. lbs. Non Lubricated. Minimum Stud Torque 30 in. lbs. Non Lubricated.

III. Maximum Ratings

A. Temperature

- Storage Temperature Range T_{stg} -65°C to +175°C
 Operating Case Temperature Range T_C -65°C to +175°C

B. Voltage

- See Table I
- 2. V_{CBO} See Table I
- $v_{EBO} = 15V D.C.$
- See Table I (Same as V_{CEO(sus)})

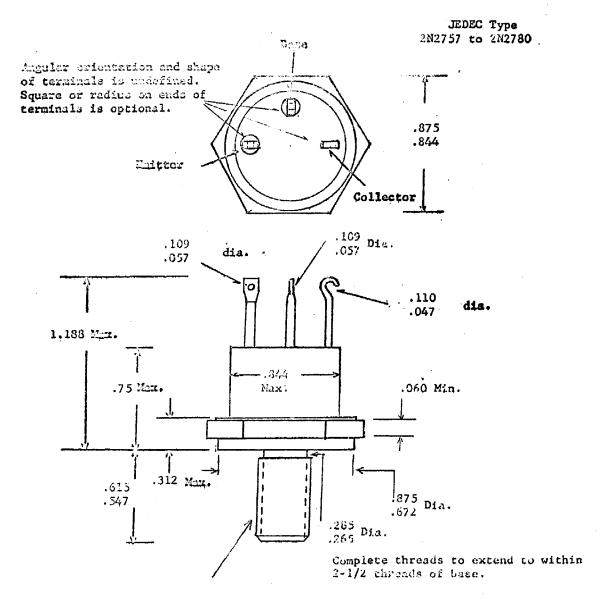
C. Current

- Continuous Collector Current 30 a.
- Continuous Base Current 7.5 Amps.
- Maximum Continuous Power Dissipation 200 Watts at a T_C of 75°C Max.
 - Linear Derating Factor 2.0W/C

NJ Semi-Conductors reserves the right to change test conditions, parameter limits and package dimensions without notice. Information furnished by NJ Semi-Conductors is believed to be both accurate and reliable at the time of going to press. However, NJ Semi-Conductors assumes no responsibility for any errors or omissions discovered in its use. NJ Semi-Conductors encourages customers to verify that datasheets are current before placing orders.

Quality Semi-Conductors





5/16-24 UNF-2A Plated Thread

.2854/.2806 Pisch Dia.

Ref. Screw Thread Standards for Federal Services Handbook 528, 1957 Pl